

TO: : **Examiner Lee**
Fax No. : **703-308-7722**
FROM : **Robert D. Watson, Agent for Applicants**
Applicant : **Peterson**
Application No. : **09/955,722**
Filed : **9/18/01**
Title : **Temporary Coatings for Protection of Microelectronic
Devices During Packaging**
Art Unit : **2815**
Examiner : **Eugene Lee**
Docket No. : **SD-6436.1**

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Applicant : Peterson #3
Application No. : 09/955,722 Election
Filed : 9/18/01
Title : Temporary Coatings for Protection of Microelectronic
Devices During Packaging
Art Unit : 2815
Examiner : Eugene Lee
Docket No. : SD-6436.1
J. McClintic
8/9/02

August 8, 2002

Commissioner for Patents
Washington DC 20231

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Response to Restriction Requirement

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Sir:

Claims 1-34 are pending in this application.

In response to the restriction requirement in the Office Action of 7/15/02, applicants elect the invention of **Embodiment II** (Fig. 2A-2E), Claims 1-25 and Claims 28-30, *with traverse*.

Embodiment II (Fig. 2A-2D) illustrates an example of a wafer with a temporary protective coating. Embodiment I (Fig. 1A-1D and Fig. 2F) illustrates an example of a die with a temporary protective coating. Dies are pieces of wafers that have been sliced and diced into smaller parts. In this sense, a "die" is a subset of a wafer.

Claims 25-27 are drawn to a die with a temporary protective coating, i.e., Embodiment I (Fig. 1A-1D and Fig. 2F). Since a die is a piece or subset of a wafer, Embodiment I and II describe a single invention, namely, a wafer, or part of a wafer, with a temporary protective coating.

Hence, a single search of the prior art should be sufficient to cover both embodiments adequately, without placing an undue burden on the Examiner.

Accordingly, applicant's request that Claims 25-27 be examined along with elected Claims 1-25 and 28-30.

Respectfully submitted,

Robert D. Watson 8/8/02

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Date: August 8, 2002

By: Elizabeth J. Greene